

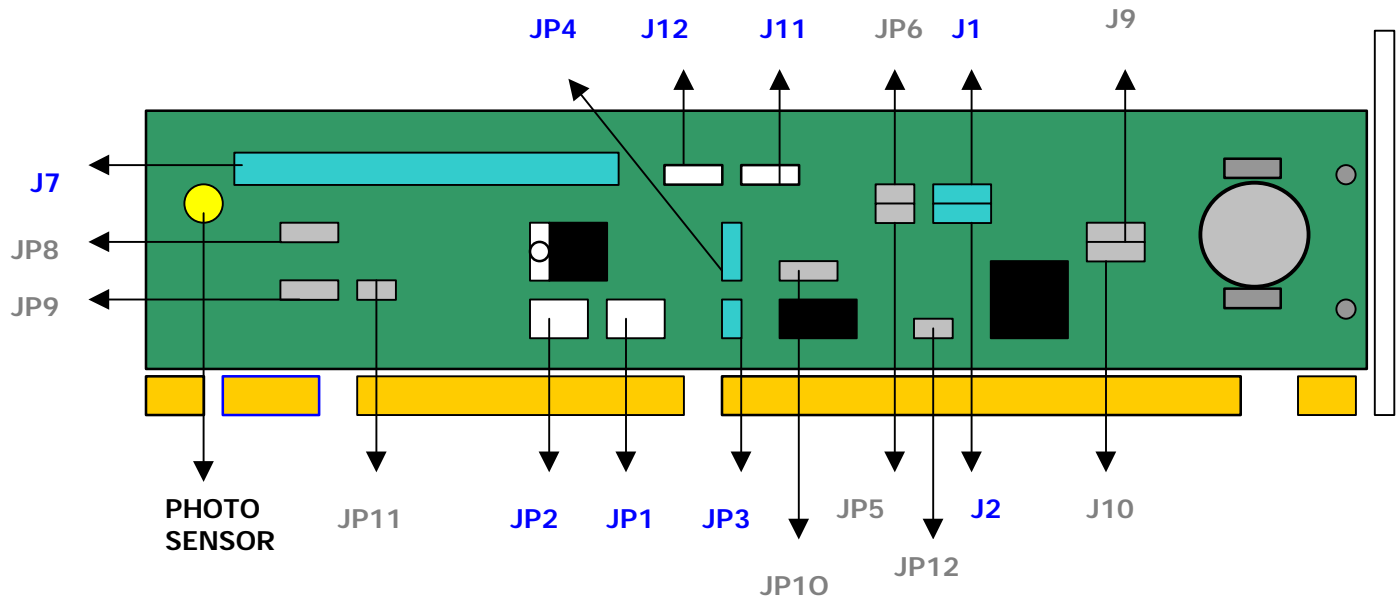
**SUPER MICRO SERVER MANAGEMENT CARD FUNCTIONAL DESCRIPTION**

**Evaluation Board Functional Description:**

The Super Micro System Management Module (SMM1) is design to monitor your PC system and alert users when hardware system resources are not stable

1. Chassis temperature sensing and intrusion detection.
2. Redundant Power Supply Failure detection, UPS active detection.
3. 5 positive voltage monitors (VCORE1, VOCRE2, 3.3V, 5V, 12V) and two negative voltage monitors (-5V, -12V)
4. Micro-switch detectors input
5. I<sup>2</sup>C serial bus interface
6. Thermal Control Circuit to turn on/off Backup Fan at specific temperature.

## Jumper Settings



*The Grey Shaded jumpers will be obsolete in production*

<b>JP1</b>	<b>Over Heat FAN Connector</b>
1	+12 V
2	GND

<b>JP2</b>	<b>Over Heat Alarm Connector</b>
1	+12 V
2	GND

<b>JP3</b>	<b>Over Heat LED Connector</b>
1	+12 V
2	GND

<b>JP4</b>	<b>Over Heat Temperature Sensor Setting</b>
1-2	50 degree C

2-3	60 degree C
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JP5	NMI_IRQ# (feed back to VID0 input)
	For Debug purpose, will be obsolete in production

JP6	SMI_OUT# (feed back to VID1 input)
	For Debug purpose, will be obsolete in production

JP8	UPS Active signal Polarity Select (will be obsolete)
1-2	Signal Active High
2-3	Signal Active Low

JP9	Redundant Power Supply Failure signal Polarity Select (will be obsolete)
1-2	Signal Active High
2-3	Signal Active Low

JP10	LM78 External Decode Address Select
1-2	For Debug purpose, will be obsolete in production

JP11	Chassis Micro Switch Input ( feedback to VID2)
	For Debug purpose, will be obsolete in production

JP12	APC UPS ON ( feedback to VID3)
	For Debug purpose, will be obsolete in production

<b>J1</b>	<b>LM78 NMI / IRQ Output Select</b>
<b>1-2</b>	<b>IRQ9</b>
<b>2-3</b>	<b>IRQ3</b>

<b>J2</b>	<b>LM78 SMI OUT# Output Select</b>
<b>1-2</b>	<b>IRQ5</b>
<b>2-3</b>	<b>IRQ7</b>

<b>J7</b>	<b>40-pin External Feature Connector</b>		
<b>Pin No.</b>	<b>Meaning</b>	<b>Pin No.</b>	<b>Meaning</b>
<b>1</b>	<b>CPU1 VCORE</b>	<b>2</b>	<b>CPU Over Heat</b>
<b>3</b>	<b>CPU2 VCORE</b>	<b>4</b>	<b>+3.3 V In</b>
<b>5</b>	<b>Reset Out #</b>	<b>6</b>	<b>SMI Out #</b>
<b>7</b>	<b>Power By Pass #</b>	<b>8</b>	<b>NMI_IRQ Out #</b>
<b>9</b>	<b>Tachometer 1 In</b>	<b>10</b>	<b>NC</b>
<b>11</b>	<b>NC</b>	<b>12</b>	<b>NC</b>
<b>13</b>	<b>Tachometer 1 In</b>	<b>14</b>	<b>Ground</b>
<b>15</b>	<b>Tachometer 2 In</b>	<b>16</b>	<b>I<sup>2</sup>C Clock In</b>
<b>17</b>	<b>Tachometer 3 In</b>	<b>18</b>	<b>I<sup>2</sup>C Data</b>
<b>19</b>	<b>NC</b>	<b>20</b>	<b>NC</b>
<b>21</b>	<b>NC</b>	<b>22</b>	<b>NC</b>
<b>23</b>	<b>CPU VID0</b>	<b>24</b>	<b>Ground</b>
<b>25</b>	<b>CPU VID1</b>	<b>26</b>	<b>Ground</b>
<b>27</b>	<b>CPU VID2</b>	<b>28</b>	<b>Ground</b>
<b>29</b>	<b>CPU VID3</b>	<b>30</b>	<b>Ground</b>
<b>31</b>	<b>CPU VID4</b>	<b>32</b>	<b>Ground</b>

33	NC	34	Ground
35	NC	36	Ground
37	CPU Over Heat	38	Rednt Power Fail In
39	Micro Switch In #	40	UPS Active In

J9	Redundant Power Supply Failure In / - 12V selection
	For engineer debug purpose, will be obsolete in production

J10	UPS Active In / - 5V selection
	For engineer debug purpose, will be obsolete in production

J11	Tachometer FAN Connector 2
1	Ground
2	+12V
3	Tachometer output 2

J12	Tachometer FAN Connector 1
1	Ground
2	+12V
3	Tachometer output 1