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# Classic R-Series Platforms

## Jumper Settings / Connectors

(\* denotes default setting; a label on top of the 5¼" drive bay shows jumper locations)

### J11 - CACHE SIZE SELECTION

|   |   |   |   |
|---|---|---|---|
| 7 | 5 | 3 | 1 |
| 8 | 6 | 4 | 2 |

| Jumper Block |             |     |     |     |                      |
|--------------|-------------|-----|-----|-----|----------------------|
| Cache Size   | 1-2         | 3-4 | 5-6 | 7-8 | SRAM Required        |
| 0 K          | Do Not Care |     |     |     | No SRAM Installed    |
| 64 K         | Out         | Out | Out | Out | 8K x 8 (both banks)  |
| 128K         | In          | Out | In  | Out | 32K x 8 (bank 0)     |
| 256K*        | In          | In  | Out | In  | 32K x 8 (both banks) |

Table B-1. Jumper J11 Options

### J12 - FLASH WRITE

1-2\* Enable +12V to FLASH (Erasable)    2-3 Disable +12V to FLASH

### J13 - FLASH BOOT BLOCK (RECOVERY MODE ENABLE)

1-2\* Boot from standard BIOS    2-3 Boot from Boot Block (recovery mode)

### J16 - ONBOARD VIDEO

1-2\* Enable onboard video    2-3 Disable onboard video

### J17 - FLOPPY WRITE PROTECT

1-2 Floppy write protected    2-3\* Floppy writable

### J18, J19 - SET CPU SPEED

Note: Use J18-J19 only for a speed change when installing an upgrade processor. Changing the speed of the main processor can damage the component.

|        | J18 | J19 |
|--------|-----|-----|
| 25 MHz | 1-2 | 1-2 |
| 33 MHz | 1-2 | 2-3 |

Table B-2. Jumper J18 and Jumper J19 Options.

### J20 - PASSWORD JUMPER

1-2 Disable and clear password    2-3\* Enable password

### J21 - CMOS SETUP PROTECTION

1-2\* Allow user to enter Setup    2-3 Prevent user from entering Setup

## J22, J23, J24, J25 - MAIN CPU SELECTION

| CPU TYPE       | J22 | J23 | J24 | J25 |
|----------------|-----|-----|-----|-----|
| i486 SX (PGA)  | 1-2 | 2-3 | 2-3 | 2-3 |
| i486 SX (PQFP) | 2-3 | 1-2 | 1-2 | 1-2 |
| i486 DX or DX2 | 2-3 | 1-2 | 1-2 | 1-2 |

Table B-3. Factory Default Main CPU Selection Jumpers.

## J28 - CLEAR CMOS JUMPER

1-2\* Installed (Don't clear CMOS)

1-2 Removed (Clear CMOS upon power-up)

## Connectors

### VIDEO PORT (J1)

| Pin | Signal Name |
|-----|-------------|
| 1   | Red         |
| 2   | Green       |
| 3   | Blue        |
| 4   | No Connect  |
| 5   | Ground      |

| Pin | Signal Name |
|-----|-------------|
| 6   | Ground      |
| 7   | Ground      |
| 8   | Ground      |
| 9   | No Connect  |
| 10  | Ground      |

| Pin | Signal Name     |
|-----|-----------------|
| 11  | No Connect      |
| 12  | No Connect      |
| 13  | Horizontal Sync |
| 14  | Vertical Sync   |
| 15  | No Connect      |

### PARALLEL PORT (J2)

| Pin | Signal Name | Direction      |
|-----|-------------|----------------|
| 1   | Strobe*     | Output         |
| 2   | Data Bit 0  | Bi-Directional |
| 3   | Data Bit 1  | Bi-Directional |
| 4   | Data Bit 2  | Bi-Directional |
| 5   | Data Bit 3  | Bi-Directional |
| 6   | Data Bit 4  | Bi-Directional |
| 7   | Data Bit 5  | Bi-Directional |
| 8   | Data Bit 6  | Bi-Directional |
| 9   | Data Bit 7  | Bi-Directional |

| Pin     | Signal Name    | Direction |
|---------|----------------|-----------|
| 10      | ACK*           | Input     |
| 11      | BUSY           | Input     |
| 12      | PE (Paper End) | Input     |
| 13      | SLCT           | Input     |
| 14      | Auto Feed XT   | Output    |
| 15      | ERROR*         | Input     |
| 16      | INIT*          | Output    |
| 17      | SLCT IN        | Output    |
| 18 - 25 | Ground         |           |

### SERIAL PORTS (J3=COM1, J4=COM2)

|   |                            |   |                            |   |                        |
|---|----------------------------|---|----------------------------|---|------------------------|
| 1 | DCD* (Data Carrier Detect) | 4 | DTR* (Data Terminal Ready) | 7 | RTS* (Request to Send) |
| 2 | SIN (Serial Input)         | 5 | Ground                     | 8 | CTS* (Clear to Send)   |
| 3 | SOUT (Serial Output)       | 6 | DSR* (Data Set Ready)      | 9 | RI* (Ring Indicator)   |

### MOUSE/KEYBOARD PORTS (J5=MOUSE, J6=KEYBOARD)

|   |            |
|---|------------|
| 1 | Data       |
| 2 | No Connect |
| 3 | Ground     |

|   |            |
|---|------------|
| 4 | Vcc        |
| 5 | Clock      |
| 6 | No Connect |

### POWER CONNECTOR (J8 & J9)

| <i>J8 Pin</i> | <i>Name</i> | <i>Function</i> |
|---------------|-------------|-----------------|
| 1             | PWRGD       | Power Good      |
| 2             | +5 V        | + 5 volts       |
| 3             | +12 V       | + 12 volts      |
| 4             | -12 V       | - 12 volts      |
| 5             | GND         | Ground          |
| 6             | GND         | Ground          |

| <i>J9 Pin</i> | <i>Name</i> | <i>Function</i> |
|---------------|-------------|-----------------|
| 1             | GND         | Ground          |
| 2             | GND         | Ground          |
| 3             | -5 V        | - 5 volts       |
| 4             | +5 V        | + 5 volts       |
| 5             | +5 V        | + 5 volts       |
| 6             | +5 V        | + 5 volts       |

### **VESA CONNECTOR (J10)**

| <i>Pin(s)</i>        | <i>Signal Name</i> |
|----------------------|--------------------|
| 1,3,5,15,17,19,21,26 | Ground             |
| 2,4,6,8,10,12,14,16  | Data 0:7           |
| 7                    | Data Enable        |
| 9                    | Sync Enable        |

| <i>Pin(s)</i> | <i>Signal Name</i> |
|---------------|--------------------|
| 11            | PCLK Enable        |
| 18            | PCLK               |
| 20            | BLANK*             |
| 13,23         | Vcc                |

### **IDE CONNECTOR (J17)**

| <i>Pin</i> | <i>Signal Name</i> |
|------------|--------------------|
| 1          | Reset IDE          |
| 2          | Ground             |
| 3          | Host Data 7        |
| 4          | Host Data 8        |
| 5          | Host Data 6        |
| 6          | Host Data 9        |
| 7          | Host Data 5        |
| 8          | Host Data 10       |
| 9          | Host Data 4        |
| 10         | Host Data 11       |
| 11         | Host Data 3        |
| 12         | Host Data 12       |
| 13         | Host Data 2        |

| <i>Pin</i> | <i>Signal Name</i> |
|------------|--------------------|
| 14         | Host Data 13       |
| 15         | Host Data 1        |
| 16         | Host Data 14       |
| 17         | Host Data 0        |
| 18         | Host Data 15       |
| 19         | Ground             |
| 20         | Key                |
| 21         | IOCHRDY            |
| 22         | Ground             |
| 23         | I/O Write          |
| 24         | Ground             |
| 25         | I/O Read           |
| 26         | Ground             |

|    |               |
|----|---------------|
| 27 | IOCHRDY       |
| 28 | BALE          |
| 29 | Reserved      |
| 30 | Ground        |
| 31 | IRQ14         |
| 32 | IOCS16        |
| 33 | Addr 1        |
| 34 | Ground        |
| 35 | Addr 0        |
| 36 | Addr 2        |
| 37 | Chip Select 0 |
| 38 | Chip Select 1 |
| 39 | Activity      |
| 40 | Ground        |

### **FLOPPY CONNECTOR (J18)**

| <i>Pin</i> | <i>Signal Name</i> |
|------------|--------------------|
| 2          | Reduced Write*     |
| 4          | Reserved           |
| 6          | Reserved           |
| 8          | Index              |
| 10         | Motor Enable A     |
| 12         | Drive Select B     |
| 14         | Drive Select A     |
| 16         | Motor Enable B     |
| 18         | DIR                |

|    |      |
|----|------|
| 20 | STEP |
|----|------|

|    |                 |
|----|-----------------|
| 22 | Write Data      |
| 24 | Write Gate      |
| 26 | Track 00        |
| 28 | Write Protect   |
| 30 | Read Data       |
| 32 | Sid 1 Select    |
| 34 | Diskette Change |

|   |        |
|---|--------|
| 1,3,7,9,11,13,15,17,<br>19,21,23,25,27,29,<br>31,33 | Ground |
|---|--------|

|   |     |
|---|-----|
| 5 | key |
|---|-----|

**AUXILLARY 5V FAN CONNECTOR (J26)**

|   |     |
|---|-----|
| 1 | +5V |
| 2 | Key |

|   |        |
|---|--------|
| 3 | Ground |
| 4 | Ground |

**HARD DRIVE LED CONNECTOR (J29)**

|   |            |
|---|------------|
| 1 | LED_HD +5V |
| 2 | HD ACTIVE  |

|   |        |
|---|--------|
| 3 | Key    |
| 4 | Ground |

**POWER LED CONNECTOR (J30)**

| Pin | Signal Name |
|-----|-------------|
| 1   | LED_PWR +5V |
| 2   | Key         |

|   |          |
|---|----------|
| 3 | Ground   |
| 4 | Reserved |
| 5 | Ground   |

**RESET CONNECTOR (J31)**

|   |       |
|---|-------|
| 1 | RESET |
|---|-------|

|   |        |
|---|--------|
| 2 | Ground |
|---|--------|

**AUXILLARY 12V FAN CONNECTOR (J32)**

| Pin | Signal Name |
|-----|-------------|
| 1   | Ground      |

|   |        |
|---|--------|
| 2 | +12V   |
| 3 | Ground |

**EXTERNAL SPEAKER CONNECTOR (J33)**

|   |      |
|---|------|
| 1 | SPK3 |
|---|------|

|   |      |
|---|------|
| 3 | SPK4 |
|---|------|

|            |   |
|------------|---|
| TITLE:     | Classic R-Series Jumper Settings / Connectors |
| INTEL REF: | R_TPS4F.doc                                   |
| PRODUCT:   | Classic R-Series                              |
| DATE/VER:  | 10/12/93, Ver. 3.0                            |

|   |     |
|---|-----|
| 2 | Key |
|---|-----|

|   |     |
|---|-----|
| 4 | +5V |
|---|-----|