P2XBL

DIP Switch Settings of the Processors

Processor		Frequency Ratio	SW1	Processor		Frequency Ratio	SW1
66MHz	100MHz			66MHz	100MHz		
233MHz	350MHz	3.5x		400MHz	Future processor	6x	
266MHz	400MHz	4x		433MHz	Future processor	6.5x	
300MHz	450MHz	4.5x		Future processor	Future processor	7x	
333MHz	500MHz	5x		Future processor	Future processor	7.5x	
366MHz	Future processor	5.5x		Future processor	Future processor	8x	

Black rectangle denotes the part that is protruding, the "adjustable" switch

SW1

In the example above:

Switch 1: Off
Switch 2: Off
Switch 3: On
Switch 4: On

Note: The voltage regulator will automatically be set according to the voltage of the processor.

JP1 (Wake-On-Keyboard/Mouse) - 2-3 On: Enable; 1-2 On: Disable (default)
By default, JP1 is disabled. Make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is also disabled. If JP1 was previously enabled with a password set in the "KB Power On Password" field, and now you wish to disable the Wake-On-Keyboard (password) function, make sure to set the "Keyboard/Mouse Power On" field to Disabled prior to setting JP1 to disabled. You will not be able to boot up the system if you fail to do so.

JP3 (CPU's FSB Select) - 1-2 On: Auto (default); 2-3 On: 66MHz; 1-2-3 Off: 100MHz

JP4 (Clear CMOS Data) - 1-2 On: Normal (default); 2-3 On: Clear CMOS Data
Before clearing the CMOS data, make sure to power-off your system and unplug the power cord.