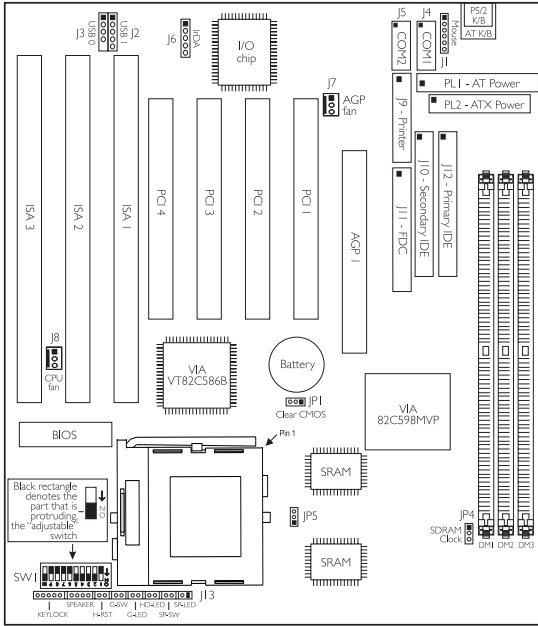


K6BV3+

rev. A+



Voltage	JP5	SW1: 7-10
2.1V		
2.2V		
2.3V		
2.4V		
2.5V		
2.8V*		
2.9V		
3.2V		
3.3V		
3.5V		

AMD - SW1: 1-6	
K6-2/300 - 66MHz - 4.5x	
K6-2/300 - 100MHz - 3x	
K6-2/333 - 66MHz - 5x	
K6-2/333 - 95MHz - 3.5x	
K6-2/350 - 100MHz - 3.5x	
K6-2/366 - 66MHz - 5.5x	
K6-2/380 - 95MHz - 4x	
K6-2/400 - 100MHz - 4x	
K6-2/450 - 100MHz - 4.5x	
K6-2/475 - 95MHz - 5x	
K6-2/500 - 100MHz - 5x	

AMD - SW1: 1-6	
K6-III/400 - 100MHz - 4x	
K6-III/450 - 100MHz - 4.5x	
K6-III/475 - 95MHz - 5x	
K6-III/500 - 100MHz - 5x	

IDT - SW1: 1-6	
WinChip2-200 - 66MHz - 3x	
WinChip2-225 - 75MHz - 3x	
WinChip2-233 - 66MHz - 3.5x	
WinChip2-266 - 100MHz - 2.33x	
WinChip2-300 - 100MHz - 2.5x	

Cyrix - SW1: 1-6	
M II-200 - 66MHz - 2.5x	
M II-266 - 66MHz - 3x	
M II-300 - 66MHz - 3.5x	
M II-300 - 75MHz - 3x	
M II-333 - 66MHz - 4x	
M II-333 - 75MHz - 3.5x	
M II-333 - 83MHz - 3x	
M II-366 - 83MHz - 3.5x	
M II-366 - 100MHz - 2.5x	
M II-380 - 100MHz - 3x	
M II-400 - 95MHz - 3.5x	

Intel - SW1: 1-6	
MMX166MHz* - 66MHz - 2.5x	
MMX200MHz - 66MHz - 3x	
MMX233MHz - 66MHz - 3.5x	

* denotes default setting

Jumper JP1 (CMOS Clear) - 1-2 On: Clear CMOS Data; 2-3 On: Normal (default)

If you are using an ATX power supply, make sure to power-off your system and unplug the power cord before clearing the CMOS data.

Jumper JP4 (SDRAM Clock) - 1-2 On: SDRAM CLK = AGP CLK (default); 2-3 On: SDRAM CLK = CPU CLK

Note: Please refer to the user's manual for the settings of older CPUs.