

Classic/PCI Expandable Desktop Memory Map, I/O Map, Interrupts & DMA

PLEASE NOTE

This motherboard product is no longer being manufactured by Intel.

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Memory Map

<i>Address Range (dec)</i>	<i>Address Range (hex)</i>	<i>Size</i>	<i>Description</i>
1024K - 65536K	100000 - 3FFFFFFF	64512 KB	Extended Memory
960K - 1023K	F0000 - FFFFF	64 KB	AMI System BIOS
952K - 959K	EE000 - EFFFF	8 KB	Unused, Available
948K - 951K	ED000 - EDFFF	4 KB	BIOS Reserved
944K - 947K	EC000 - ECFFF	4 KB	Unused, Available
930K - 943K	E8800 - EBFFF	14 KB	Unused, Available
800K - 929K	C8000 - E87FF	130 KB	Unused, Available (open to the PCI and ISA bus)
768K - 799K	C0000 - C7FFF	32 KB	Video BIOS area
640K - 767K	A0000 - BFFFF	128 KB	Video Display Memory
639K	9FC00 - 9FFFF	1 KB	Video BIOS Data (moveable by QEMM, 386MAX)
512K - 638K	80000 - 9FBFF	127 KB	Extended Conventional
0K - 511K	00000 - 7FFFF	512 KB	Conventional

Table D-1. Classic/PCI Expandable Desktop memory map

I/O Map

<i>Address(hex)</i>	<i>Size(Dec)</i>	<i>Description</i>	<i>Address(hex)</i>	<i>Size(Dec)</i>	<i>Description</i>
0000 - 000F	16 bytes	IB - DMA 1	02F8 - 02FF	8 bytes	On-Board Serial Port 2*
0020 - 0021	2 bytes	IB - Interrupt Controller 1	0376	1 byte	Secondary IDE Channel
0040 - 0043	4 bytes	IB - Timer 1	0377	1 byte	Secondary IDE Channel Status
0060	1 byte	Keyboard Controller Data Byte	0378 - 037F	8 bytes	Parallel Port 1*
0061	1 byte	IB - NMI status and control	03BC - 03BF	4 bytes	Parallel Port x*
0064	1 byte	Keyboard status and control	03E8 - 03EF	8 bytes	Serial Port 3*
0070, bit 7	1 bit	IB - Enable NMI	03F0 - 03F5	6 bytes	Floppy Channel 1
0070, bits 6:0	7 bits	Real Time Clock, Address	03F6	1 bytes	Primary IDE Channel Command
0071	1 byte	Real Time Clock, Data	03F7 (Write)	1 byte	Floppy Channel 1 Command
0078	1 byte	Reserved - Board Configuration	03F7, bit 7	1 bit	Floppy Disk Change Channel 1
0080 - 009F	16 bytes	IB - DMA Page Register	03F7, bits 6:0	7 bits	Primary IDE Channel Status
00A0 - 00A1	2 bytes	IB - Interrupt Controller 2	03F8 - 03FF	8 bytes	On-Board Serial Port 1*
00B0	1 byte	Power management control	0481 - 048B	11 bytes	DMA high page registers
00B1	1 byte	APM status port	04D0	1 byte	Int 1 edge/level control
00C0 - 00DE	31 bytes	IB - DMA 2	04D1	1 byte	Int 2 edge/level control
00F0	1 bytes	Reset Numeric Error	0CF8-0CFB	4 bytes	PCI Config.Address Register**
0170 - 0177	8 bytes	Secondary IDE Channel	0CF9	1 byte	Deturbo Mode Enable
01F0 - 01F7	8 bytes	Primary IDE Channel	0CFC-0CFF	4 bytes	PCI Config. Data Register**
0278 - 027B	4 bytes	Parallel Port 2*			

Table E-1. Classic/PCI Expandable Desktop I/O address map

* Configurable in CMOS setup

**Only accessible by DWORD accesses.

Interrupts & DMA Channels

<i>IRQ</i>	<i>System Resource</i>
NMI	Parity Error
0	Reserved, Interval Timer
1	Reserved, Keyboard buffer full
2	Reserved, Cascade interrupt from slave PIC
3	Serial Port 2
4	Serial Port 1
5	User Available
6	Floppy
7	Parallel Port 1
8	Real Time Clock
9	User available
10	User available
11	User available
12	User available
13	Reserved, Math coprocessor
14	PCI Local Bus IDE
15	User available

Table F-1. Classic/PCI Expandable Desktop interrupts

<i>DMA</i>	<i>Data Width</i>	<i>System Resource</i>
0	8-bits	Open
1	8-bits	Open
2	8-bits	Floppy
3	8-bits	Open
4		Reserved - Cascade channel
5	16-bits	Open
6	16-bits	Open
7	16-bits	Open

Table F-2. Classic/PCI Expandable Desktop DMA map